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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
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10/754,557

01/12/2004

Hitoshi Komeno

HITA.0491

8212

7590

10/05/2004

REED SMITH LLP

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EXAMINER

LEE, SEUNG H

ART UNIT

PAPER NUMBER

2876

DATE MAILED: 10/05/2004

Please find below and/or attached an Office communication concerning this application or proceeding.

Office Action Summary

Application No.

10/754,557

Applicant(s)

KOMENO ET AL.

Examiner

Seung H Lee

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-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If the period for reply specified above is less than thirty (30) days, a reply within the statutory minimum of thirty (30) days will be considered timely.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

Status

- 1) ☐ Responsive to communication(s) filed on ____.
- 2a) ☐ This action is **FINAL**. 2b) ☒ This action is non-final.
- 3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

Disposition of Claims

- 4) ☒ Claim(s) 1-24 is/are pending in the application.
- 4a) Of the above claim(s) ____ is/are withdrawn from consideration.
- 5) ☐ Claim(s) ____ is/are allowed.
- 6) ☒ Claim(s) 1-24 is/are rejected.
- 7) ☐ Claim(s) ____ is/are objected to.
- 8) ☐ Claim(s) ____ are subject to restriction and/or election requirement.

Application Papers

- 9) ☐ The specification is objected to by the Examiner.
- 10) ☐ The drawing(s) filed on ____ is/are: a) ☐ accepted or b) ☐ objected to by the Examiner.
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).
Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).
- 11) ☐ The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

Priority under 35 U.S.C. § 119

- 12) ☒ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
- a) ☒ All b) ☐ Some * c) ☐ None of:
1. ☐ Certified copies of the priority documents have been received.
 2. ☒ Certified copies of the priority documents have been received in Application No. 10/235,890.
 3. ☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).

* See the attached detailed Office action for a list of the certified copies not received.

Attachment(s)

- | | |
|--|---|
| 1) <input type="checkbox"/> Notice of References Cited (PTO-892) | 4) <input type="checkbox"/> Interview Summary (PTO-413)
Paper No(s)/Mail Date. ____. |
| 2) <input type="checkbox"/> Notice of Draftsperson's Patent Drawing Review (PTO-948) | 5) <input type="checkbox"/> Notice of Informal Patent Application (PTO-152) |
| 3) <input checked="" type="checkbox"/> Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08)
Paper No(s)/Mail Date <u>1/12/2004</u> . | 6) <input type="checkbox"/> Other: ____. |

DETAILED ACTION

Prelim. Amdt./Amendment

1. Receipt is acknowledged of the Preliminary Amendment filed on 06 May 2004.

Double Patenting

The nonstatutory double patenting rejection is based on a judicially created doctrine grounded in public policy (a policy reflected in the statute) so as to prevent the unjustified or improper timewise extension of the "right to exclude" granted by a patent and to prevent possible harassment by multiple assignees. See *In re Goodman*, 11 F.3d 1046, 29 USPQ2d 2010 (Fed. Cir. 1993); *In re Longi*, 759 F.2d 887, 225 USPQ 645 (Fed. Cir. 1985); *In re Van Ornum*, 686 F.2d 937, 214 USPQ 761 (CCPA 1982); *In re Vogel*, 422 F.2d 438, 164 USPQ 619 (CCPA 1970); and, *In re Thorington*, 418 F.2d 528, 163 USPQ 644 (CCPA 1969).

A timely filed terminal disclaimer in compliance with 37 CFR 1.321(c) may be used to overcome an actual or provisional rejection based on a nonstatutory double patenting ground provided the conflicting application or patent is shown to be commonly owned with this application. See 37 CFR 1.130(b).

Effective January 1, 1994, a registered attorney or agent of record may sign a terminal disclaimer. A terminal disclaimer signed by the assignee must fully comply with 37 CFR 3.73(b).

Obviousness-Type Double Patenting

2. Claims 1-24 are rejected under the judicially created doctrine of obviousness-type double patenting as being unpatentable over claims 1-24 of US Patent No. 6,710,839. Although the conflicting claims are not identical, they are not patentably distinct from each other because in claims 1-24 of the instant application, Applicants claim a display device comprising a substrate having a display region and picture frame regions which are arranged outside the display region and surround the display region, a plurality of data lines which extend in the first direction and are arranged in parallel in

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the second direction which crosses the first direction on the display region of the substrate, a plurality of gate lines which extend in the second direction and are arranged in parallel in the first direction on the display region of the substrate, a plurality of pixels which are arranged in a matrix and have a storage capacity in each pixel, and a plurality of storage lines which extend in the second direction, are arranged in parallel in the first direction alternately with the gate lines in the display region of the substrate, and supply electricity to the storage capacity wherein the substrate includes a plurality of connection terminals which are formed on a first side and are connected to an external circuit, first and second gate-line pull-around lines which are respectively formed on the picture frame regions at second and third sides close to the first side and pull out the plurality of gate lines in the direction toward the first side, and first and second common lines which are respectively formed on the picture frame regions at the second and third sides and connect the plurality of storage lines each other, a gate wiring pattern which is constituted of the plurality of gate lines and the first and second gate-line pull-around lines and a storage wiring pattern which is constituted of the plurality of storage lines and the first and second common lines are formed into wiring patterns which do not cross each other.

Claims 1-24 of the US Patent No. 6,710,839 and disclose a display device comprising: a substrate having a display region and picture frame regions which are arranged outside the display region and surround the display region, a plurality of data lines which extend in the first direction and are arranged in parallel in the second direction which crosses the first direction on the display region of the substrate, a

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plurality of gate lines which extend in the second direction and are arranged in parallel in the first direction on the display region of the substrate, switching elements which are formed in the vicinity of crossing points between the data lines and the gate lines, pixel electrodes each of which is formed in a region which is surrounded by the neighboring data lines and the neighboring gate lines, and a plurality of storage lines which extend in the second direction, are arranged in parallel in the first direction alternately with the gate lines, and form storage capacities between the pixel electrodes and the storage lines in the display region of the substrate, the improvement being characterized in that the substrate includes a plurality of connection terminals which are formed on a first side and are connected to an external circuit, first and second gate-line pull-around lines which are respectively formed on the picture frame regions at second and third sides close to the first side and pull out the plurality of gate lines in the direction toward the first side, and first and second common lines which are respectively formed on the picture frame regions at the second and third sides and connect the plurality of storage lines each other, a gate wiring pattern which is constituted of the plurality of gate lines and the first and second gate-line pull-around lines and a storage wiring pattern which is constituted of the plurality of storage lines and the first and second common lines are formed into wiring patterns which do not cross each other.

Therefore, as discussed above, the scope of claims 1-24 of the present application and claims 1-24 of US Patent No. 6,710,839 are practically identical.

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Conclusion


Any inquiry concerning this communication or earlier communication from the examiner should be directed to Seung H. Lee whose telephone number is (571) 272-2401. The examiner can normally be reached on Monday to Friday from 7:30 AM to 4:00 PM.


If attempt to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Michael G. Lee, can be reached on (571) 272-2398. The fax-phone number for this group is (703) 872-9306.

Communications via Internet e-mail regarding this application, other than those under 35 U.S.C. 132 or which otherwise require a signature, may be used by the applicant and should be addressed to [seung.lee@uspto.gov].

All Internet e-mail communications will be made of record in the application file. PTO employees do not engage in Internet communications where there exists a possibility that sensitive information could be identified or exchanged unless the record includes a properly signed express waiver of the confidentiality requirements of 35 U.S.C. 122. This is more clearly set forth in the Interim Internet Usage Policy published in the Official Gazette of the Patent and Trademark on February 25, 1997 at 1195 OG 89.

Any inquiry of a general nature or relating to the status of this application or proceeding should be directed to the Group receptionist whose telephone number is (703) 308-0956.


Seung H. Lee
Art Unit 2876
September 27, 2004


MICHAEL G. LEE
SUPERVISORY PATENT EXAMINER
TECHNOLOGY CENTER 2800